WHAT IS CLAIMED IS:

1. An operational amplifier circuit with compensation for parasitic capacitances associated with input circuit resistors and feedback circuit resistors, comprising:

a first semiconductor element between a first input circuit resistor and a substrate;

a second semiconductor element between a second, feedback circuit resistor and said substrate;

a first electrical connection connecting said first semiconductor element to a voltage input of said operational amplifier circuit; and

a second electrical connection connecting said second semiconductor element to a voltage output of said operational amplifier circuit.

2. The circuit of claim 1 wherein said first and second semiconductor elements are n-wells.

3. The circuit of claim 2 wherein said resistors are polysilicon.

4. The circuit of claim 1 further comprising:

a plurality of additional resistors;

a plurality of separate semiconductor elements, each of said separate semiconductor elements being between one of said additional resistors and said substrate;

a first switching circuit for connecting each of said semiconductor elements to one of said voltage input and said voltage output.

5. The circuit of claim 4 further comprising:

a second switching circuit for programmably connecting each of said additional resistors into one of said input circuit and said feedback circuit;

whereby a first group of said semiconductor elements, which correspond to additional resistors connected into said feedback circuit, are connected to said voltage output; and

whereby a second group of said semiconductor elements, which correspond to additional resistors connected into said input circuit, are connected to said voltage input.

6. The circuit of claim 5 further comprising:

a logic circuit, connected to said second switching circuit, and configured to detect a position of a switch connection in said second switching circuit and control said first switching circuit to provide a desired switch connection of said first switching circuit.

7. An operational amplifier circuit with compensation for parasitic capacitances associated with resistors of an input circuit and a feedback circuit, comprising:

a plurality of resistors;

a plurality of separate semiconductor elements, each of said separate semiconductor elements being between one of said resistors and said substrate;

a first switching circuit for connecting each of said semiconductor elements to one of a voltage input and a voltage output of said operational amplifier circuit;

a second switching circuit for programmably connecting each of said resistors into one of said input circuit and said feedback circuit;

whereby a first group of said semiconductor elements, which correspond to resistors connected into said feedback circuit, are connected to said voltage output; and

a second group of said semiconductor elements, which correspond to additional resistors connected into said input circuit, are connected to said voltage input.

8. The circuit of claim 7 wherein said semiconductor elements are n-wells.

9. An operational amplifier circuit with compensation for parasitic capacitances associated with resistors of an input circuit and a feedback circuit, comprising:

a plurality of resistors;

a plurality of separate semiconductor elements, each of said separate semiconductor elements being between one of said resistors and said substrate;

a first switching circuit for connecting each of said semiconductor elements to one of a voltage input and a voltage output of said operational amplifier circuit;

a second switching circuit for programmably connecting each of said resistors into one of said input circuit and said feedback circuit;

whereby a first group of said semiconductor elements, which correspond to resistors connected into said feedback circuit, are connected to said voltage output;

a second group of said semiconductor elements, which correspond to additional resistors connected into said input circuit, are connected to said voltage input; and

a logic circuit, connected to said second switching circuit, and configured to detect a position of a switch connection in said second switching circuit and control said first switching circuit to provide a desired switch connection of said first switching circuit.

10. A method for compensating for parasitic capacitances associated with input circuit resistors and feedback circuit resistors in an operational amplifier circuit, comprising:

providing a first semiconductor element between a first input circuit resistor and a substrate;

providing a second semiconductor element between a second, feedback circuit resistor and said substrate;

connecting said first semiconductor element to a voltage input of said operational amplifier circuit; and

connecting said second semiconductor element to a voltage output of said operational amplifier circuit.

11. The method of claim 10 further comprising:

providing a plurality of additional resistors;

providing a plurality of separate semiconductor elements, each of said separate semiconductor elements being between one of said additional resistors and said substrate; and

connecting each of said semiconductor elements to one of said voltage input and said voltage output.

12. The method of claim 11 further comprising:

programmably connecting each of said additional resistors into one of said input circuit and said feedback circuit;

whereby a first group of said semiconductor elements, which correspond to additional resistors connected into said feedback circuit, are connected to said voltage output; and

whereby a second group of said semiconductor elements, which correspond to additional resistors connected into said input circuit, are connected to said voltage input.

13. The method of claim 10 wherein said semiconductor elements are n-wells.